



Docket No.: 1076.1094

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Takashi KURIHARA, et al.

Serial No. 10/823,649

Group Art Unit: 2825

Confirmation No. 4917

Filed: April 14, 2004

Examiner: DOAN, NGHIA M.

For: METHOD AND APPARATUS FOR DESIGNING SEMICONDUCTOR INTEGRATED
CIRCUIT

AMENDMENT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed March 28, 2007, and having a period for response set to expire on June 28, 2007. A Petition for a one-month extension of time, together with the requisite fee for same, is submitted herewith, thereby extending the period for response to July 28, 2007.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.